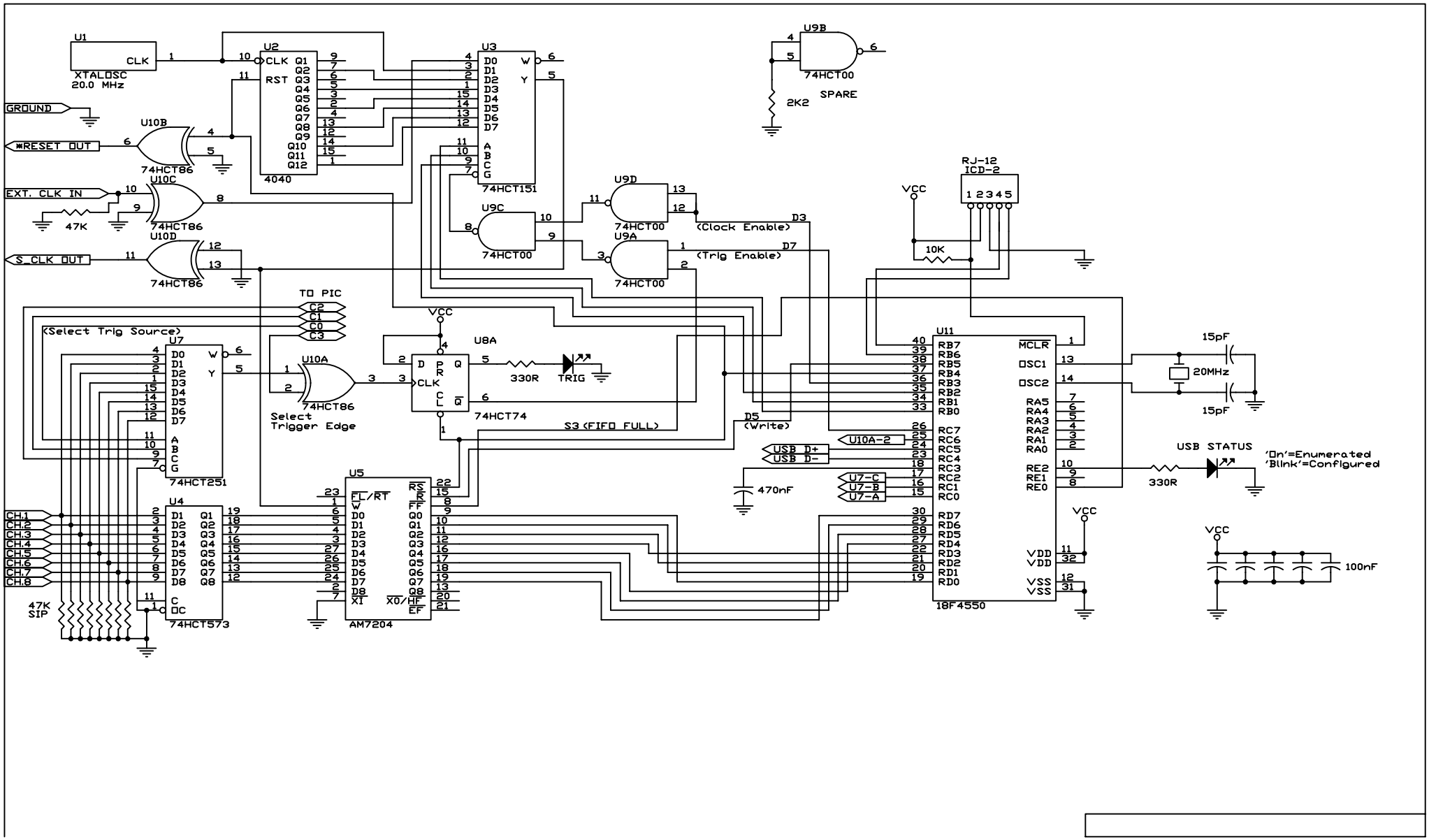
U1
CLK
XTALOSC
20.0 MHz
U2
CLK
RST
Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8 Q9 Q10 Q11 Q12
4040
U3
D0 D1 D2 D3 D4 D5 D6 D7
W
Y
A B C G
74HCT151
U9B
74HCT00
SPARE
2K2
GROUND
U10B
*RESET OUT
74HCT86
U10C
EXT. CLK IN
47K
74HCT86
U10D
S_CLK OUT
74HCT86
U9C
74HCT00
U9D
74HCT00
U9A
74HCT00
(Clock Enable)
D3
(Trig Enable)
D7
RJ-12
ICD-2
1 2 3 4 5
VCC
10K
TO PIC
C2
C1
C0
C3
(Select Trig Source)
U7
74HCT251
U10A
74HCT86
Select
Trigger Edge
VCC
U8A
D
PR
Q
CLK
CL
74HCT74
330R
TRIG
S3 (FIFO FULL)
D5
(Write)
U11
MCLR
OSC1
OSC2
RA5 RA4 RA3 RA2 RA1 RA0
RE2 RE1 RE0
RB7 RB6 RB5 RB4 RB3 RB2 RB1 RB0
RC7 RC6 RC5 RC4 RC3 RC2 RC1 RC0
RD7 RD6 RD5 RD4 RD3 RD2 RD1 RD0
VDD VDD
VSS VSS
18F4550
U10A-2
USB D+
USB D-
U7-C
U7-B
U7-A
470nF
15pF
20MHz
15pF
USB STATUS
330R
'On'=Enumerated
'Blink'=Configured
VCC
100nF
U4
74HCT573
CH.1
CH.2
CH.3
CH.4
CH.5
CH.6
CH.7
CH.8
47K
SIP
U5
AM7204
FL/RT
W
D0 D1 D2 D3 D4 D5 D6 D7 D8
XI
RS
R
FF
Q0 Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8
XO/HF
EF